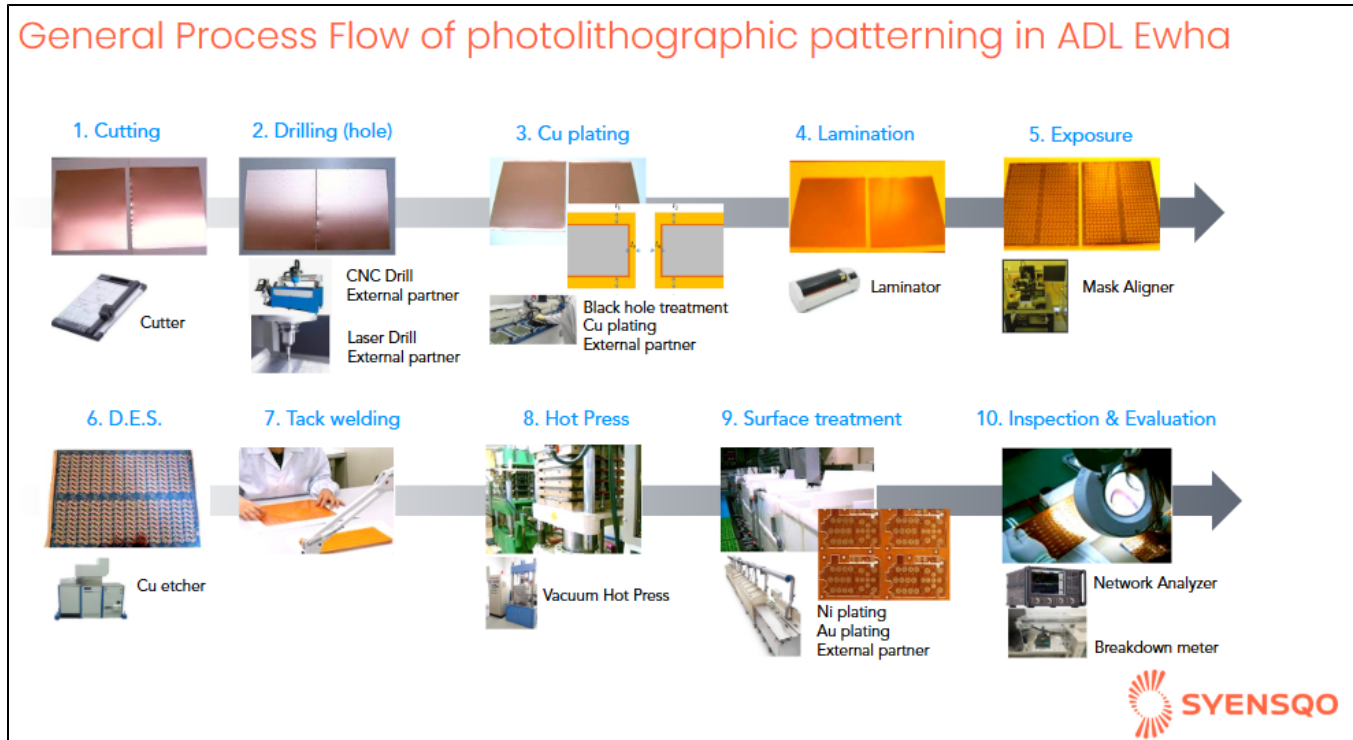


IC Substrate Manufacturing

IC substrate manufacturing is the complex process of creating a **carrier board for integrated circuits (ICs)**, involving multi-layer lamination of copper and resin, precise laser drilling of vias (connections), copper plating and etching to form fine circuit patterns, and the application of surface finishes for protection and soldering. This process enables the crucial mechanical and electrical link between the tiny IC chip and a larger printed circuit board (PCB).



Key Steps in IC Substrate Manufacturing

- 1. Design:** Creating a detailed blueprint for the intricate network of traces, pads, and vias.
- 2. Core Material Lamination:** Bonding together thin layers of copper foil and resin (like glass fiber and epoxy) under heat and pressure to form a solid, stable base.
- 3. Via Drilling:** Using precise laser or mechanical drilling to create tiny vertical holes (vias) that connect different layers of the substrate.
- 4. Copper Plating & Etching:** Depositing conductive copper into the drilled vias and onto the surfaces, then etching away unwanted copper to define the circuit pathways.

Solder Mask & Surface Finish: Applying a protective solder mask and a surface finish (like ENIG or ENEPIG) to prevent oxidation and facilitate soldering.

Testing: Rigorous final inspection and testing to ensure the substrate meets all performance, dimensional, and functional requirements.

Purpose of an IC Substrate

- **Interconnection:** Acts as a bridge, translating the tiny, dense connections of an IC chip to the larger pads of a motherboard or PCB.
- **Support & Protection:** Provides a mechanical base to protect the delicate IC from damage and stress.
- **Heat Dissipation:** Helps conduct heat away from the chip, improving performance and longevity.
- **Signal Integrity:** Designed with advanced materials and precise routing to ensure reliable signal transmission, especially crucial for high-speed applications.

Why It's Different from Standard PCB Manufacturing

IC substrate manufacturing uses more **advanced technologies**, such as the **Modified Semi-Additive Process (MSAP)**, to create much finer line widths and spacings. This allows for greater density and complexity, necessary for modern, high-performance CPUs, GPUs, and other advanced ICs.